

WEST Search History

DATE: Friday, May 14, 2004

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
		<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L18	L17 and (adding near device)	0
<input type="checkbox"/>	L17	L15 and (management near2 control)	60
<input type="checkbox"/>	L16	L15 and (power?builder or PowerBuilder or power builder)	1
<input type="checkbox"/>	L15	345/734-743.ccls.	743
<input type="checkbox"/>	L14	L12 and (management near3 control)	1
<input type="checkbox"/>	L13	L12 and (management near3 control\$)	2
<input type="checkbox"/>	L12	L1 and l2	27
		<i>DB=JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L11	L5 and DDE	1
		<i>DB=USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L10	L2 and DDE	22
<input type="checkbox"/>	L9	L3 and intelligent	9
<input type="checkbox"/>	L8	L3 and DDE	0
		<i>DB=JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L7	L5 and (adding near device)	13
<input type="checkbox"/>	L6	L5 and (power?builder or PowerBuilder or power builder)	2
<input type="checkbox"/>	L5	power near (management or control)	42696
		<i>DB=USPT,PGPB; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L4	L3 and (power?builder or PowerBuilder or power builder)	0
<input type="checkbox"/>	L3	L2 and (adding near device)	135
<input type="checkbox"/>	L2	power near (management or control)	71732
		<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L1	717/100-113,120-122,168-170.ccls.	2237

END OF SEARCH HISTORY

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Search Results - Record(s) 1 through 20 of 27 returned.

1. Document ID: US 20040088678 A1

Using default format because multiple data bases are involved.

L12: Entry 1 of 27

File: PGPB

May 6, 2004

PGPUB-DOCUMENT-NUMBER: 20040088678
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040088678 A1

TITLE: System and method for visualizing process flows

PUBLICATION-DATE: May 6, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Litoiu, Marin	Toronto		CA	
Storey, Margaret	Victoria		CA	
Rayside, Derek	Aurora		CA	

US-CL-CURRENT: 717/104; 345/763

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KWC](#) [Drawn Desc](#) [In](#)

2. Document ID: US 20040015941 A1

L12: Entry 2 of 27

File: PGPB

Jan 22, 2004

PGPUB-DOCUMENT-NUMBER: 20040015941
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040015941 A1

TITLE: Information-processing apparatus equipped with nonvolatile memory device and firmware-updating method for use in the apparatus

PUBLICATION-DATE: January 22, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Sekine, Norihisa	Ome-shi		JP	

US-CL-CURRENT: 717/168; 713/2, 713/300

ABSTRACT:

An information-processing apparatus includes a nonvolatile memory device configured to store firmware. The information-processing apparatus has a first unit for issuing an instruction to make an operating system execute a shutdown process, and to update the firmware, stored in the nonvolatile memory device, after the operating system has completed the shutdown process. The information-processing apparatus also has a second unit, responsive to the instruction to update the firmware, for updating the firmware only after the operating system has completed the shutdown process.

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KWMC](#) | [Drawn Desc](#) | [In](#)

3. Document ID: US 20040015822 A1

L12: Entry 3 of 27

File: PGPB

Jan 22, 2004

PGPUB-DOCUMENT-NUMBER: 20040015822

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040015822 A1

TITLE: Method and apparatus for dynamic assembly and verification of software components into flexible applications

PUBLICATION-DATE: January 22, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Linton, Samuel W.	Sunnyvale	CA	US	
Wallerius, John W.	Sunnyvale	CA	US	
Sussman, Myles A.	Sunnyvale	CA	US	

US-CL-CURRENT: 717/104; 717/108, 717/116

ABSTRACT:

A method and apparatus for assembling software components into an application. In an embodiment, the apparatus generally relates to an application creator which assembles software modules at run time into a container application. The container application supports interface inheritance and implementation inheritance from an existing software component.

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KWMC](#) | [Drawn Desc](#) | [In](#)

4. Document ID: US 20040005859 A1

L12: Entry 4 of 27

File: PGPB

Jan 8, 2004

PGPUB-DOCUMENT-NUMBER: 20040005859

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040005859 A1

TITLE: Wireless deployment / distributed execution of graphical programs to smart

sensors

PUBLICATION-DATE: January 8, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ghercioiu, Marius	Austin	TX	US	
Ceteras, Ciprian	Baia Mare	TX	RO	
Monoses, Ioan	Gherla jud Cluj		RO	
Crisan, Gratian I.	jud Cluj		RO	
Kodosky, Jeffrey L.	Austin		US	

US-CL-CURRENT: 455/3.01; 717/109

ABSTRACT:

System and method for deploying or executing a graphical program to a device in a wireless manner. A graphical program (GP) is created that implements a measurement function. Some or all of the GP is transmitted to a hub over a network. The hub executes the transmitted GP and sends corresponding commands to a measurement device via wireless means in accordance with a wireless communication protocol. The measurement device executes the commands to perform the measurement function, thereby generating resultant data, which is received from the measurement device via wireless means. The GP may include a block diagram that executes on the measurement device, and a user interface portion that is displayed by a first computer system. Transmitting the GP to the hub may include generating a machine-executable program based on the GP and transmitting the machine-executable program to the hub for execution.

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KOMC](#) | [Draw Desc](#) | [In](#)

5. Document ID: US 20030233646 A1

L12: Entry 5 of 27

File: PGPB

Dec 18, 2003

PGPUB-DOCUMENT-NUMBER: 20030233646

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030233646 A1

TITLE: Image based installation

PUBLICATION-DATE: December 18, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Cohen, Jason	Seatac	WA	US	
Burkhardt, Ryan	Redmond	WA	US	
Miller, Wesley G.	Snohomish	WA	US	
Yaryan, Tom	Seattle	WA	US	
Green, Bruce L.	Sammamish	WA	US	

US-CL-CURRENT: 717/174; 717/168

ABSTRACT:

A system and method for copying a run-time image independent of computer context from a source computer-readable medium to one or more target computer-readable media of a target computer. Software of the invention integrates the copied software product with the target computer. The software applies an operating system and/or associated application programs to the target computer as a run-time image.

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KWDIC](#) | [Drawn Desc](#) | [In](#)

6. Document ID: US 20030192028 A1

L12: Entry 6 of 27

File: PGPB

Oct 9, 2003

PGPUB-DOCUMENT-NUMBER: 20030192028

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030192028 A1

TITLE: System and method for determining software object migration sequences

PUBLICATION-DATE: October 9, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Gusler, Carl Phillip	Austin	TX	US	
Hamilton, Rick Allen II	Charlottesville	VA	US	
O'Higgins, James	Toronto		CA	
Verbeek, Ronald Andrew	Ancaster		CA	

US-CL-CURRENT: 717/101

ABSTRACT:

A system and method for determining software object migration sequences is presented. Objects for hardware platform migration are identified and assigned an object identifier. Decision factors and corresponding weightings are assigned which are used in determining an object migration order. Object identifier grades are determined for each decision factor corresponding to each object identifier. The object identifier grades are multiplied with corresponding decision factor weightings which results in decision factor scores. The decision factor scores for each object identifier are added together which results in a migration score for the corresponding object identifier. The migration scores along with object dependencies are used to generate a migration order. The migration order is import to a project planning software which generates a migration project plan.

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KWDIC](#) | [Drawn Desc](#) | [In](#)

7. Document ID: US 20030163508 A1

L12: Entry 7 of 27

File: PGPB

Aug 28, 2003

PGPUB-DOCUMENT-NUMBER: 20030163508
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030163508 A1

TITLE: Background code update for embedded systems

PUBLICATION-DATE: August 28, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Goodman, Brian Gerard	Tucson	AZ	US	

US-CL-CURRENT: 718/100; 708/318, 717/168, 717/170

ABSTRACT:

According to the present invention, there is provided an embedded system and method for performing a background code update of a current code image with an incoming code image in an embedded system, the method comprising the steps of: executing the current code image in the embedded system; executing one or more code update routines from the incoming code image to update the current code image with the incoming code image; and executing a task switching function from the current code image to switch microprocessor control from executing the one or more code update routines of the incoming image to execute a function in the current code image. The system and method may further provide for retrieving an offset from the incoming code image for the one or more code update routines in the incoming code image. The system and method may further provide for retrieving an offset from the current code image of a task switching function. Additionally, the embedded system may form a part of a larger system, preferably a storage automation library.

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KWC](#) | [Drawn Desc](#) | [In](#)

8. Document ID: US 20030084435 A1

L12: Entry 8 of 27

File: PGPB

May 1, 2003

PGPUB-DOCUMENT-NUMBER: 20030084435
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030084435 A1

TITLE: Method and system for offloading execution and resources for resource-constrained networked devices

PUBLICATION-DATE: May 1, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Messer, Alan	Los Gatos	CA	US	
Greenberg, Ira	Mountain View	CA	US	
Milojicic, Dejan	Palo Alto	CA	US	
Bernadat, Philippe	Varces	CA	FR	

Fu, Guangrui

Pal Alto

US

US-CL-CURRENT: 717/174; 717/168

ABSTRACT:

A method for offloading execution load from a resource-constrained device to allow the device to run a resource intensive software program. The method includes the step of accessing a program using a first device and determining whether execution of the program will exceed resources of the first device. If the execution will exceed the resources of the first device, a load split is determined for the program to split the program into a first portion and a second portion. The second portion of the program is offloaded to a second device, while the first portion of the program is retained on the first device. The first portion and second portion communicate via a network connection between the first device and the second device. Full program functionality is provided using the first device, wherein the functionality is provided by the first portion and second portion executing respectively on the first device and the second device.

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMIC](#) | [Drawn Desc](#) | [In](#)

9. Document ID: US 20030074658 A1

L12: Entry 9 of 27

File: PGPB

Apr 17, 2003

PGPUB-DOCUMENT-NUMBER: 20030074658

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030074658 A1

TITLE: Apparatus and method for upgrading program

PUBLICATION-DATE: April 17, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Kim, Gyoung-Yeol	Yongin-si		KR	

US-CL-CURRENT: 717/168; 717/174

ABSTRACT:

An apparatus and a method for upgrading a program that controls a microprocessor. The program upgrading method includes: installing a Personal Computer Memory Card International Association (PCMCIA) interface device in the system; recording the upgrading program in a PCMCIA card; downloading the upgrading program from the PCMCIA card through the PCMCIA interface device installed in the system under the control of the program stored in the memory; and upgrading the program stored in the memory with the downloaded program under the control of the program stored in the memory. The program upgrading method can perform program upgrading through a PCMCIA interface easily.

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMIC](#) | [Drawn Desc](#) | [In](#)

10. Document ID: US 20030005411 A1

L12: Entry 10 of 27

File: PGPB

Jan 2, 2003

PGPUB-DOCUMENT-NUMBER: 20030005411
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030005411 A1

TITLE: System and method for dynamic packaging of component objects

PUBLICATION-DATE: January 2, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Gerken, Christopher Henry	Austin	TX	US	

US-CL-CURRENT: 717/120; 717/168

ABSTRACT:

A system and method for dynamically packaging component objects. A server receives a request from a client. The server identifies one or more objects based on the request. The identified objects may be copied from the server's nonvolatile storage area, generated dynamically, or be a result of modifying objects stored on the server's nonvolatile storage area. The identified objects are written to a package file. The server downloads the package file to the client. A dialog may be sent to the client to determine whether the client wishes to download the package file. The package file is provided dynamically in response to the client's request. The package file may include objects that include generated code, such as Java tag handler files, Java tag extra info files, and Java tag library description information. Java definitions may be included with the package file to create a Java jar file.

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KOMC](#) | [Drawn Desc](#) | [In](#)

11. Document ID: US 20020092008 A1

L12: Entry 11 of 27

File: PGPB

Jul 11, 2002

PGPUB-DOCUMENT-NUMBER: 20020092008
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020092008 A1

TITLE: Method and apparatus for updating new versions of firmware in the background

PUBLICATION-DATE: July 11, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Kehne, Kevin Gene	Austin	TX	US	
Mehta, Chetan	Austin	TX	US	
Patel, Jayeshkumar M.	Austin	TX	US	

Patel, Kasturi

Austin TX US

US-CL-CURRENT: 717/168

ABSTRACT:

A method, system, and computer program for updating firmware in a data processing system as a background operation allowing a user to utilize the computer for other purposes during the update process is provided. In one embodiment, after an operating system has been loaded and control has been transferred from the service processor to the host, the service processor determines whether the level of a firmware copy on a system component, such as an SPCN card, matches the current level of firmware stored on a non-volatile memory accessible to the service processor. If the level of the firmware copy in the component is different from the current level, the service processor transfers the current level of firmware from the non-volatile memory accessible to the service processor to the system component.

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KWMC](#) | [Drawn Desc](#) | [In](#)

12. Document ID: US 20010008024 A1

L12: Entry 12 of 27

File: PGPB

Jul 12, 2001

PGPUB-DOCUMENT-NUMBER: 20010008024
PGPUB-FILING-TYPE: new-utility
DOCUMENT-IDENTIFIER: US 20010008024 A1

TITLE: Upgrade control method and data processing system

PUBLICATION-DATE: July 12, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Inaba, Toru	Yokohama		JP	

US-CL-CURRENT: 717/170

ABSTRACT:

The invention relates to an upgrade control method and data processing system for system operational data and software, and performs an upgrade without interrupting or stopping service. The data processing system comprises: a system operational data storing unit 7 which stores therein system operational data and version information; a system version management information storing unit 8 which stores therein system acceptable software and system version information; a database version verification unit 2 which, when performing an upgrade, verifies based on the version information whether or not the upgrade is acceptable by the system; a database conversion unit 5 which converts the format of the system operational data; and a power-up control unit 4 which issues a start-up request to a system functional group 6, and wherein: when performing an upgrade, verification is made based on the version information to determine whether or not the upgrade is acceptable by the system and, if the upgrade is acceptable, the power-up control unit 4 issues the start-up request to the system functional group 6 and the system operational data is converted to the latest format but, if the upgrade is not

acceptable, the current operational state is maintained and a mismatched state is reported.

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KWMC](#) | [Drawn Desc](#) | [In](#)

13. Document ID: US 6662355 B1

L12: Entry 13 of 27

File: USPT

Dec 9, 2003

US-PAT-NO: 6662355

DOCUMENT-IDENTIFIER: US 6662355 B1

TITLE: Method and system for specifying and implementing automation of business processes

DATE-ISSUED: December 9, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Caswell; Nathan S.	Yorktown Heights	NY		
Ciccolo; Arthur C.	Ridgefield	CT		
Nigam; Anil	Stamford	CT		

US-CL-CURRENT: 717/103; 705/1

ABSTRACT:

A method s for specifying and implementing automation of business processes where the specification is independently manipulable by both the business process owner and technical implementers, and resulting technical elements can be tested for compliance with every detail in the specification. The method creates a single shared model suitable for understanding and execution in both the business and technical domains by focusing on the specification problem in the area of business automation. The solution to the specification problem lies in Information, Function, Flow (IFF or IF.sup.2) factorization of business processes. Models of the business are constructed by way of the IF.sup.2 modeling methodology. This is a complete model which includes, by construction, external specifications of each task included in the business model. The modularization problem is solved by preserving the partitioning of the system created in the business model. The automation system implements concrete modules that uniquely and directly correspond to particular elements whose external specification is determined by the business model.

9 Claims, 5 Drawing figures

Exemplary Claim Number: 1

Number of Drawing Sheets: 5

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KWMC](#) | [Drawn Desc](#) | [In](#)

14. Document ID: US 6651241 B1

L12: Entry 14 of 27

File: USPT

Nov 18, 2003

US-PAT-NO: 6651241
DOCUMENT-IDENTIFIER: US 6651241 B1

TITLE: Scriptor and interpreter

DATE-ISSUED: November 18, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Hernandez, III; Gaspar	Andover	NJ		

US-CL-CURRENT: 717/112; 717/110, 717/115, 717/142, 717/143

ABSTRACT:

A scriptor and interpreter are disclosed. The scriptor and interpreter each not only check the syntax of a command in a script, but also that the values for the variables passed as arguments to command are consistent with the values attributed to those variables in preceding portions of the script.

28 Claims, 5 Drawing figures

Exemplary Claim Number: 1

Number of Drawing Sheets: 4

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Abstract](#) | [Claims](#) | [KIMC](#) | [Drawn Desc](#) | [In](#)

15. Document ID: US 6463373 B2

L12: Entry 15 of 27

File: USPT

Oct 8, 2002

US-PAT-NO: 6463373

DOCUMENT-IDENTIFIER: US 6463373 B2

TITLE: Fail-safe system in integrated control of vehicle

DATE-ISSUED: October 8, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Suganuma; Takeshi	Kariya			JP
Fujii; Yoshimitsu	Okazaki			JP

US-CL-CURRENT: 701/48; 303/112, 701/29, 701/33, 701/34, 701/36, 701/37, 701/38, 701/43,
701/45, 701/49, 717/168

ABSTRACT:

A fail-safe system used in integrated control of a vehicle includes structural-element control portions. Preset priority degrees are given to the structural-element control portions, respectively. A manager control portion stores one or more substitute programs designed to implement functions of ones among the structural-element control portions which are necessary for travel of the vehicle. A downloading device operates for, when

one of the structural-element control portions which is necessary for travel of the vehicle fails, selecting one from non-failed ones of the structural-element control portions as a download destination in accordance with the priority degrees and downloading the substitute program corresponding to the failed structural-element control portion into the selected download-destination structural-element control portion. The selected download-destination structural-element control portion is lower in priority degree than the failed structural-element control portion. The download-destination structural-element control portion serves for the failed structural-element control portion according to the downloaded substitute program.

17 Claims, 8 Drawing figures

Exemplary Claim Number: 1

Number of Drawing Sheets: 6

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Abstract](#) | [Detailed](#) | [Claims](#) | [KWMC](#) | [Drawn Desc](#) | [In](#)

16. Document ID: US 6453461 B1

L12: Entry 16 of 27

File: USPT

Sep 17, 2002

US-PAT-NO: 6453461

DOCUMENT-IDENTIFIER: US 6453461 B1

TITLE: Method and apparatus for testing ASL plug and play code in an ACPI operating system

DATE-ISSUED: September 17, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Chaiken; Craig L.	Tomball	TX		

US-CL-CURRENT: 717/124; 713/1, 713/2, 717/109, 717/121

ABSTRACT:

In a first aspect, the invention is a method for interfacing a generic program with the ASL code in an ACPI system. The method comprises storing information from a generic program in a shared memory; accessing the ASL code; and retrieving the stored information with the ASL code. In a second aspect, the invention is a method for testing ASL PnP code in an ACPI system. The method comprises identifying a configurable PnP device; disabling the identified configurable PnP device; testing the disabled, configurable PnP device for a configuration; and verifying that, for the tested configuration, the resulting current resources match the set resources. In variations of these aspects, the methods may be performed by instructions encoded on a computer-readable, program storage medium and used to program a computer.

32 Claims, 11 Drawing figures

Exemplary Claim Number: 1

Number of Drawing Sheets: 5

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Abstract](#) | [Detailed](#) | [Claims](#) | [KWMC](#) | [Drawn Desc](#) | [In](#)

17. Document ID: US 6345387 B1

L12: Entry 17 of 27

File: USPT

Feb 5, 2002

US-PAT-NO: 6345387

DOCUMENT-IDENTIFIER: US 6345387 B1

TITLE: Coherent object system architecture

DATE-ISSUED: February 5, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Morrison; Gordon E.	Westminster	CO		

US-CL-CURRENT: 717/170; 717/120

ABSTRACT:

A method and apparatus are provided for controlling the execution of a software program. According to one embodiment, control flow information including multiple states associated with a software program is made accessible. Each of the states includes information indicative of desired control flow of the software program and information indicative of desired behavior of the software program. A determination is made if a current status of the software program has a predetermined relationship with an expected state of execution of the software program. Based upon the result of the determination and the information indicative of desired behavior, the software program is caused to perform an action and caused to transition from the current state to a next state. According to another embodiment, specification of a control flow architecture associated with a software program is simplified to the provision of certain control flow information that is accessible to a control flow engine. The control flow information need only include information indicative of desired control flow of the software program and information indicative of desired behavior of the software program.

72 Claims, 16 Drawing figures

Exemplary Claim Number: 1

Number of Drawing Sheets: 15

Full	Title	Citation	Front	Review	Classification	Date	Reference	Abstract	Claims	KWIC	Draw Desc	Ir
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18. Document ID: US 6178550 B1

L12: Entry 18 of 27

File: USPT

Jan 23, 2001

US-PAT-NO: 6178550

DOCUMENT-IDENTIFIER: US 6178550 B1

TITLE: Mechanism for optimizing location of machine-dependent code

DATE-ISSUED: January 23, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Pearce; John J.	Del Valle	TX		

US-CL-CURRENT: 717/100; 711/163, 713/200

ABSTRACT:

Method and apparatus for optimizing the location of machine-dependent code is disclosed. In a departure from the art, code for implementing an applications program interface (API) or other machine dependent program is divided into a system management mode (SMM) portion and a non-SMM portion, depending on whether the code needs to be implemented in a secure environment or not, respectively. In a preferred embodiment, the non-SMM portion of the API maintains a BIOS jump table comprising an indexed array of function addresses, and the SMM portion of the API maintains a parallel, but nonidentical, SMM jump table. For each API function that is to be executed in SMM, that is, for each function that must be executed in a secure environment, the indexed BIOS jump table entry calls an SMM interface (SMI) function, which initiates SMM by generating a system management interrupt and accesses the parallel SMM jump table. The indexed SMM jump table entry is used to call the designated function, which is then executed in SMM.

11 Claims, 3 Drawing figures

Exemplary Claim Number: 1

Number of Drawing Sheets: 1

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19. Document ID: US 6131187 A

L12: Entry 19 of 27

File: USPT

Oct 10, 2000

US-PAT-NO: 6131187

DOCUMENT-IDENTIFIER: US 6131187 A

TITLE: Method and system for translating exception handling semantics of a bytecode class file

DATE-ISSUED: October 10, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Chow; Chung Hen	Austin	TX		
Liao; Yu Chung	Austin	TX		

US-CL-CURRENT: 717/106; 717/118

ABSTRACT:

A method for translating exception handling semantics of a bytecode class file within a computer system is disclosed. An empty bytecode information array is first established. Pertinent information is then obtained by examining bytecodes within a bytecode stream, and such information are inserted into the bytecode information array. An exception framelist, which includes an exception handling routine, is subsequently obtained from a

class file for the bytecode stream. The entries within the bytecode information array corresponding to a starting location and an ending location of the exception framelist are marked. Finally, a high-level code sequence is generated utilizing the bytecode information array.

12 Claims, 3 Drawing figures

Exemplary Claim Number: 1

Number of Drawing Sheets: 2

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20. Document ID: US 6049672 A

L12: Entry 20 of 27

File: USPT

Apr 11, 2000

US-PAT-NO: 6049672

DOCUMENT-IDENTIFIER: US 6049672 A

TITLE: Microprocessor with circuits, systems, and methods for operating with patch micro-operation codes and patch microinstruction codes stored in multi-purpose memory structure

DATE-ISSUED: April 11, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Shiell; Jonathan H.	Plano	TX		
Bosshart; Patrick W.	Plano	TX		

US-CL-CURRENT: 717/168; 711/102, 711/108, 711/118, 712/209

ABSTRACT:

A microprocessor operates in response to microinstructions stored in a read only memory. A patch table stores a indication of patch microinstructions stored in cache memory. This cache memory caches data and/or macroinstructions for the microprocessor. Each new microaddress is compared with the patch table entries. If there in no match, then a multiplexer selects the microinstruction recalled from that microinstruction address within the microinstruction read only memory. If there is a match, then a corresponding patch microinstruction is recalled from the cache memory. The multiplexer selects this patch microinstruction. The microprocessor operates under the control of the selected microinstruction. This technique enables a fix of faulty microinstructions in the field, by supplying the computer user with the patch microinstructions. Using a portion of the cache memory to store the patch microinstructions eliminates any problem with specifying too large or too small a memory for patch microinstructions.

20 Claims, 11 Drawing figures

Exemplary Claim Number: 12

Number of Drawing Sheets: 7

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Andersen, B.;
 Cement Industry Technical Conference. 1997. XXXIX Conference Record., 1997
 IEEE/PCA , 20-24 April 1997
 Pages:97 - 112

[Abstract] [\[PDF Full-Text \(1580 KB\)\]](#) **IEEE CNF**

2 Managing power consumption in networks on chips

Simunic, T.; Boyd, S.P.; Glynn, P.;
 Very Large Scale Integration (VLSI) Systems, IEEE Transactions on , Volume:
 12 , Issue: 1 , Jan. 2004
 Pages:96 - 107

[Abstract] [\[PDF Full-Text \(488 KB\)\]](#) **IEEE JNL**

3 Power management strategy for a parallel hybrid electric truck

Chan-Chiao Lin; Huei Peng; Grizzle, J.W.; Jun-Mo Kang;
 Control Systems Technology, IEEE Transactions on , Volume: 11 , Issue: 6 , Nov.
 2003
 Pages:839 - 849

[Abstract] [\[PDF Full-Text \(700 KB\)\]](#) **IEEE JNL**

4 General configuration of automation system within a high voltage power substation

Trabulus, S.;
 Electrical and Computer Engineering, 2000 Canadian Conference on , Volume:
 2 , 7-10 March 2000
 Pages:1167 - 1171 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(448 KB\)\]](#) [IEEE CNF](#)

5 Hot-carrier reliability and design of N-LDMOS transistor arrays

Brisbin, D.; Strachan, A.; Chaparala, P.;

Integrated Reliability Workshop Final Report, 2001. 2001 IEEE International , 15-18 Oct. 2001

Pages:44 - 48

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